1. **Origin of the idea** ((Minimum font-size: 10pt, times new roman font.)
2. **Design framework or Methodology adopted for the implementation of the idea**. This should have implementation of Embedded design/ System Design /Block/RTL/Algorithm/Logic/Circuit/ Module/architecture using open or industry standard tools/IPs (Minimum font-size: 10pt, times new roman font. Max two pages including tables, diagrams, and references (if any).
3. **Application of the design** (Explain an example consisting of potential application/Future application.
4. **Advancement in the field** (Explain the major advantage of the implemented Embedded design/ System Design /Block/RTL/Algorithm/Logic/Circuit/ Module/architecture. You should describe the key value addition of your design and explain why your idea has value in the presence of other solutions.